

General Description

The 006N12 uses advanced trench technology and design to provide excellent RDS(ON) . This device is suitable for PWM, load switching and general purpose applications.

Product Summary

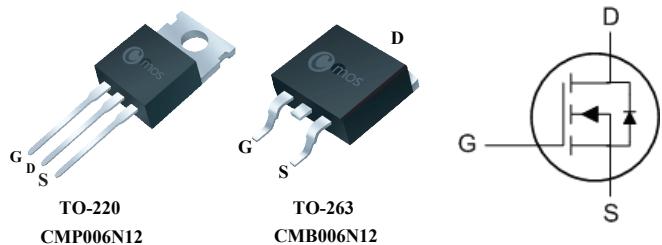
BVDSS	RDS(ON)	ID
120V	5.8mΩ	120A

Applications

- Synchronous Rectification
- Power Management in Inverter Systems
- Motor Driver

Features

- Low On-Resistance
- Reliable and Rugged
- RoHS Compliant

TO-220/263 Pin Configuration**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	120	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current	120	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current	96	A
I_{DM}	Pulsed Drain Current	360	A
EAS	Single Pulse Avalanche Energy ¹	480	mJ
P_D	Total Power Dissipation	250	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient(Steady State)	---	62.5	°C/W
$R_{\theta JC}$	Thermal Resistance Junction -Case(Steady State)	---	0.5	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	120	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	5.1	5.8	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=20\text{A}$	---	6.3	7	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D =150\mu\text{A}$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=100\text{V}$, $V_{\text{GS}} =0\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=18\text{A}$	---	34	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.5	---	Ω
Q_g	Total Gate Charge	$I_D=20\text{A}$	---	60	---	nC
Q_{gs}	Gate-Source Charge		---	7	---	
Q_{gd}	Gate-Drain Charge		---	9	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=50\text{V}$	---	14	---	ns
T_r	Rise Time		---	5	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	41	---	
T_f	Fall Time		---	7	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	4000	---	pF
C_{oss}	Output Capacitance		---	400	---	
C_{rss}	Reverse Transfer Capacitance		---	15	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	120	A
I_{SM}	Pulsed Source Current		---	---	360	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=20\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V

Notes:

1.Starting $T_J = 25^\circ\text{C}$, $L = 1\text{mH}$, $I_D = 31\text{A}$, $V_{\text{DD}} = 50\text{V}$, $V_{\text{GS}} = 10\text{V}$.

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

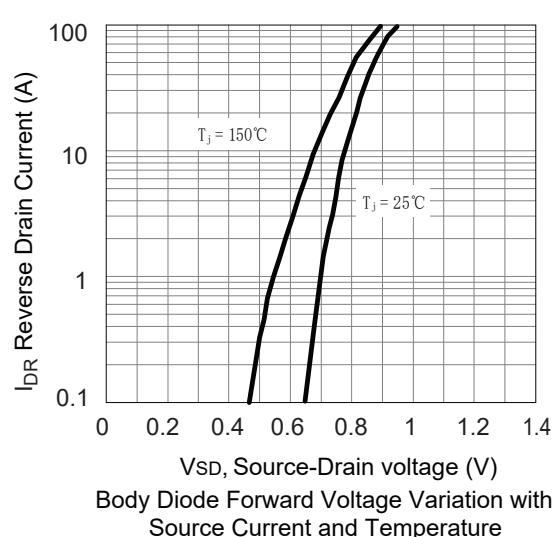
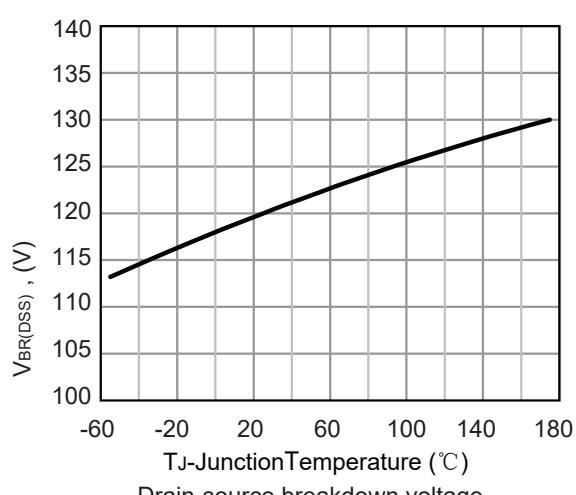
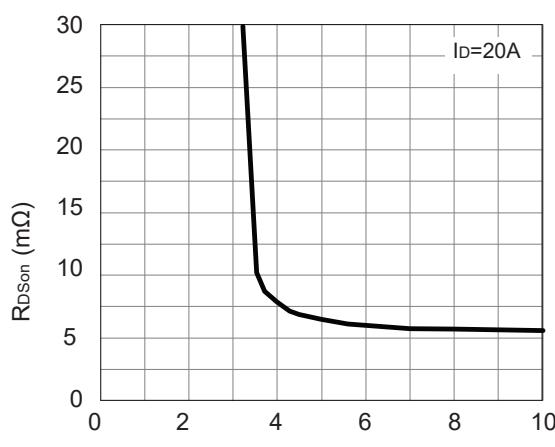
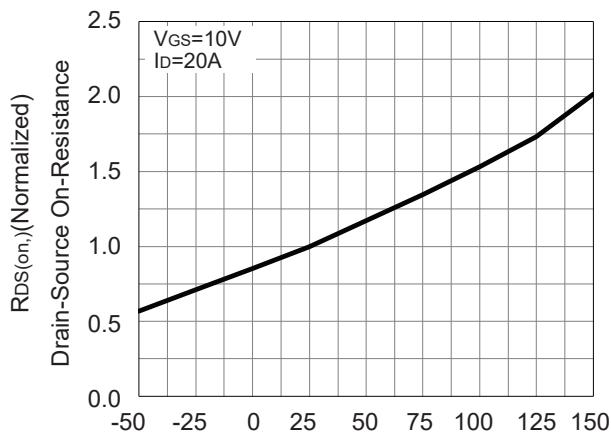
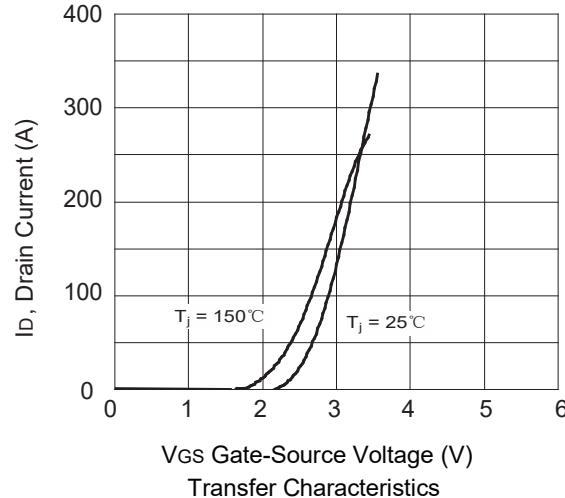
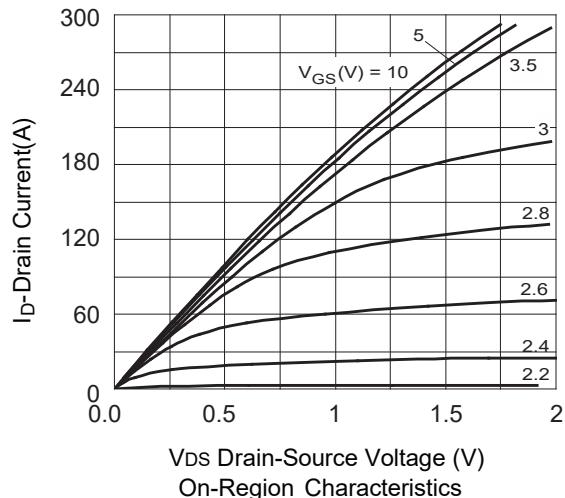
Cmos reserves the right to improve product design, functions and reliability without notice.

CMP006N12/CMB006N12



Typical Characteristics

120V N-Channel MOSFET



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